Data Acquisition Probe/Development Board

Data Acquisition Probe(DAP)/Development Board is a low cost, compact size solution for moderate speed data acquisition and development of many applications. It supports high performance reconfigurable system with onboard FPGA and microcontroller. The board facilitates high speed inter communication interfaces between FPGA and microcontroller for data processing and control logic. This board has facility to communicate with cloud through onboard Wifi/BT module and custom GUI with USB. It also supports for dedicated applications like Rotary Encoder Test Jig, CAN-FD based DAP or Multichannel Temperature Sensor interface with plug and play adapter board.

Power

Multiple powering options

- Through Expansion Header
- USB
- Power over Ethernet (PoE)

Clocking

- 50MHz Crystal Oscillator
- 60MHz FTDI Clock

Communication & Networking

- Gigabit Ethernet
- USB
- Wi-Fi/BT Interface

Expansion Connectors

- 2 SPI Master Interface
- 4 channel CANFD Interface
- 8 Pair of Differential IO Support
- Debug UART
- GPIO
- Power Supply Pins

Processing Power

- SPARTAN 7 FPGA
- Microcontroller

Memory

- SD Card (Up to 64Mb)
- 32kb EEPROM (IIC)
- 256Mb Quad SPI Flash

Configuration

- Onboard configuration circuitry
- 256Mb Quad SPI Flash
- JTAG port

Inter FPGA –Microcontroller Communication

- High Speed EBI Interface
- I2C
- GPIO

Timing

- RTC –IIC

Control & I/O

- 6X LEDs
- Power Indicator
- User Switches

Analog

- V & I Measurement capability

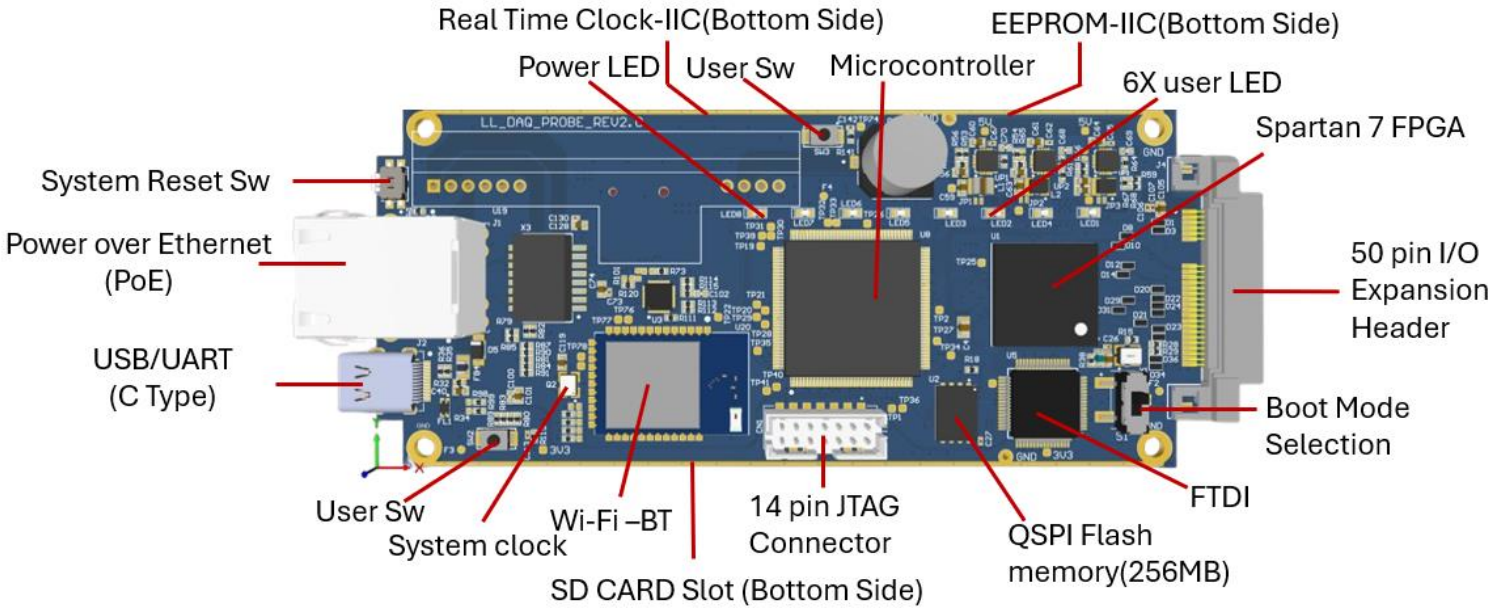

Fig : Top View